

FIGURE 1

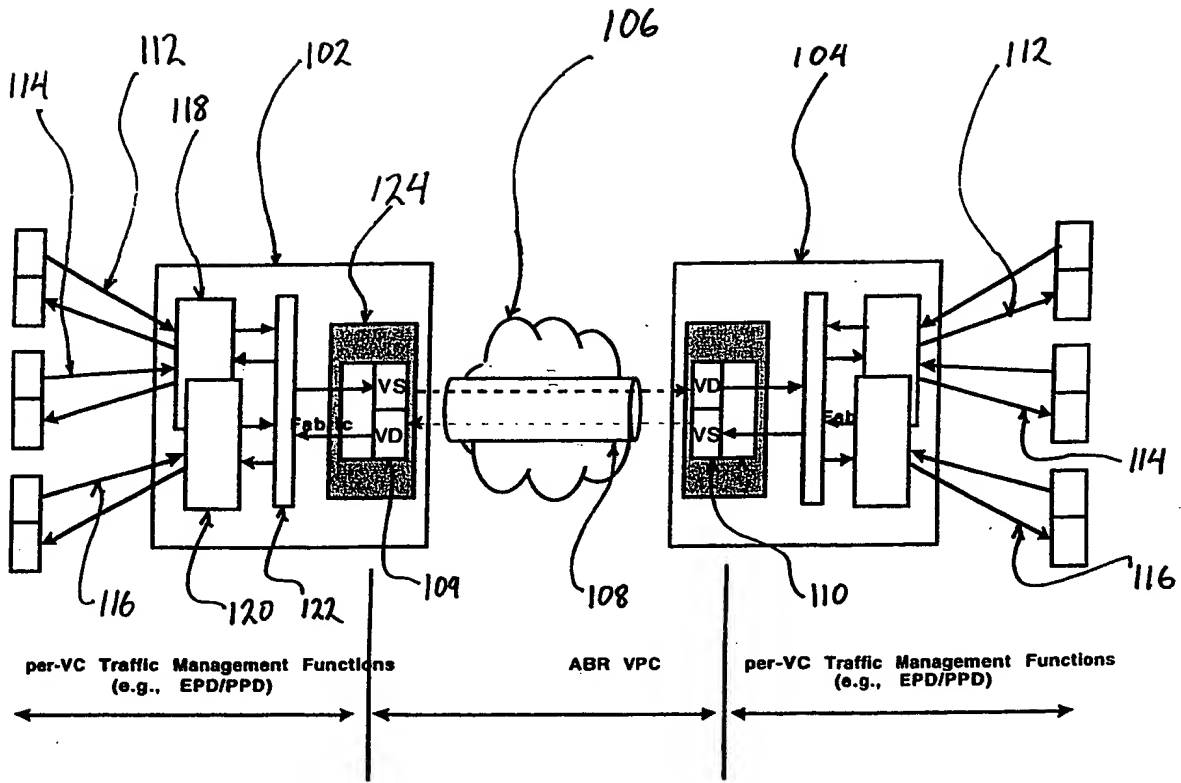


FIGURE 2

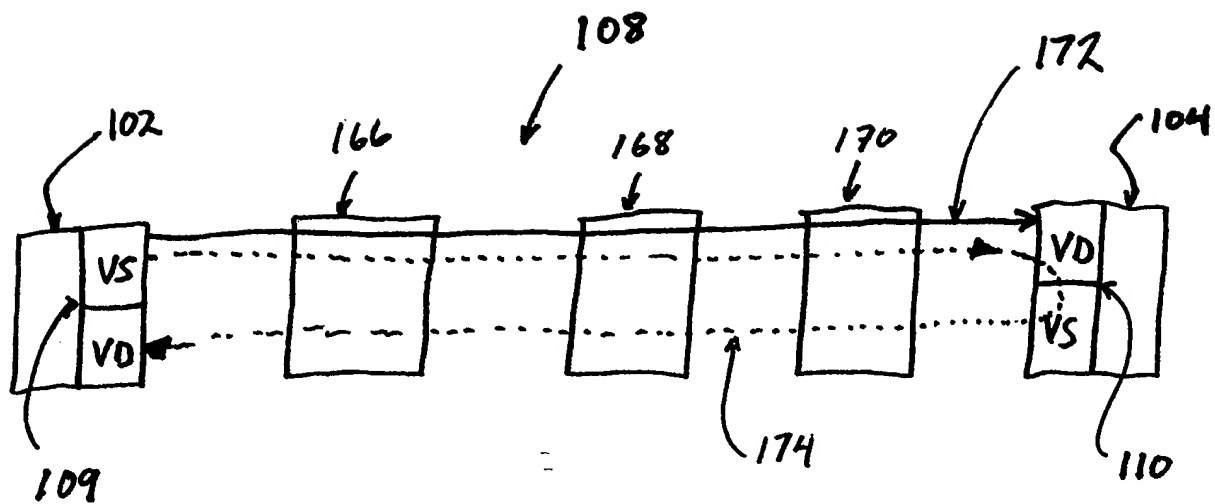


FIGURE 3

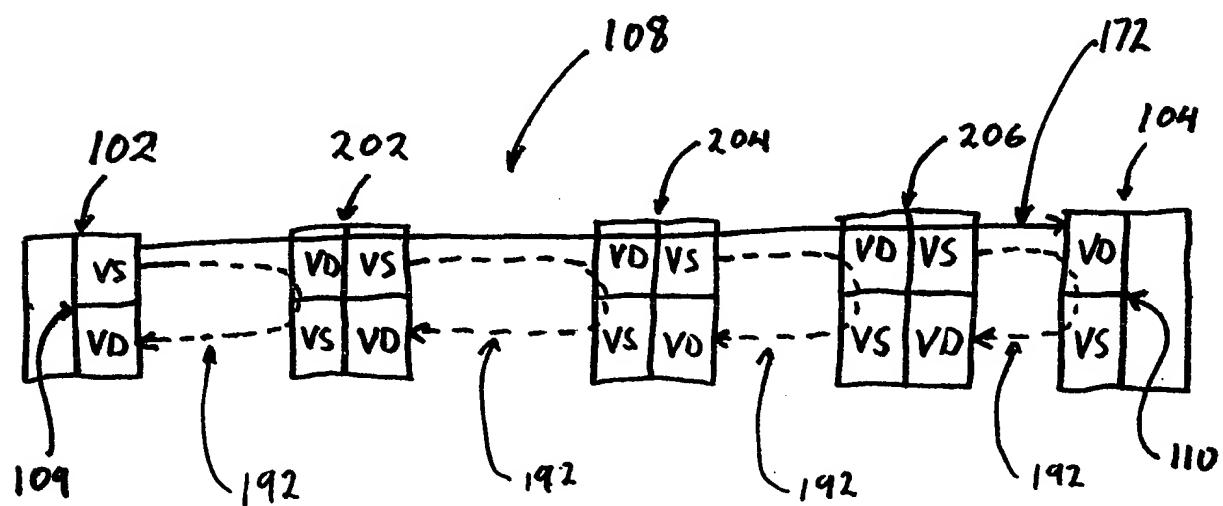
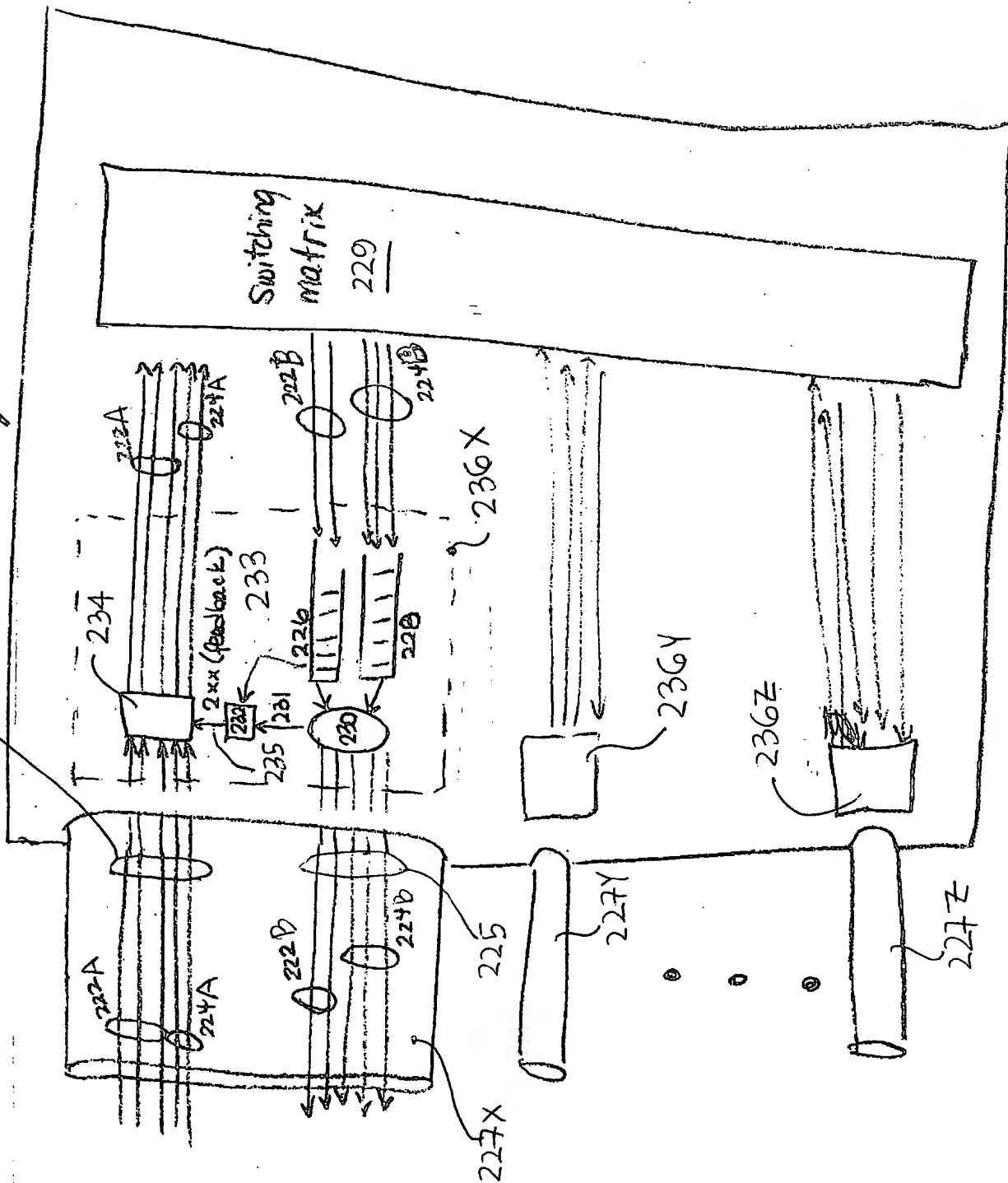


FIGURE 4

662210-48E5E260

Fig. 5



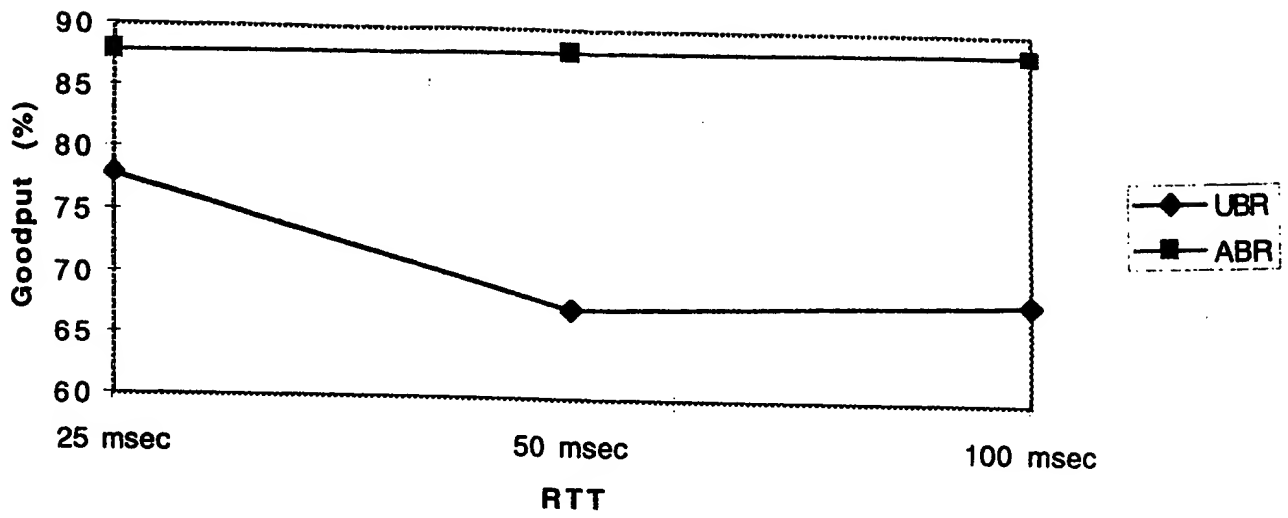


Figure 6 • Goodput Performance with Constant Switch Buffer Size

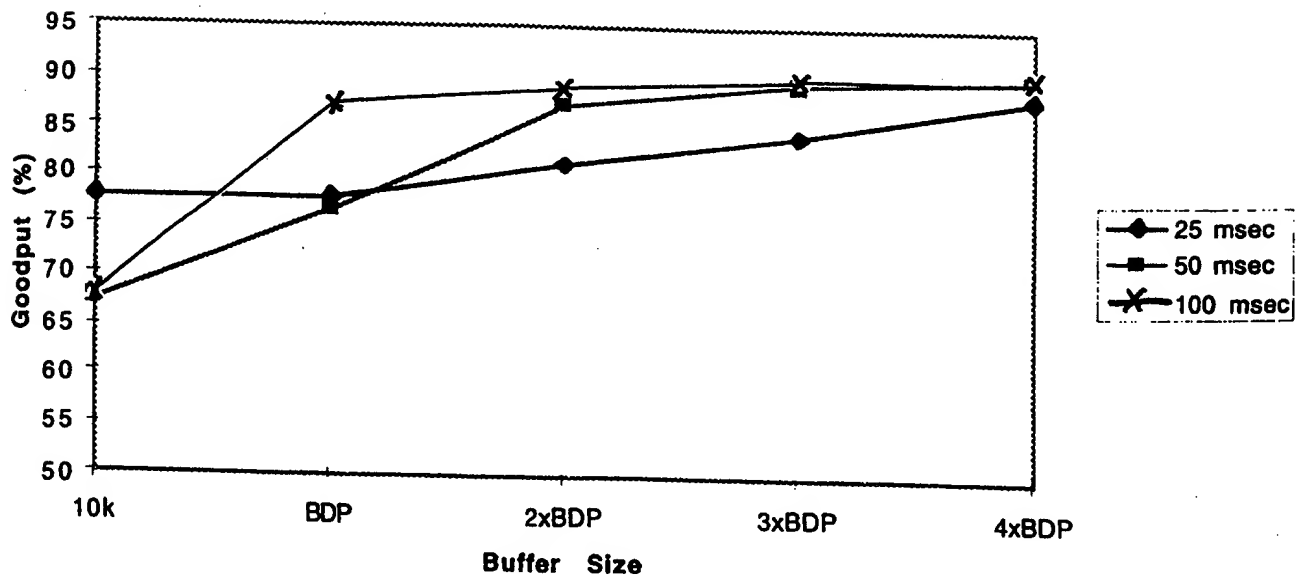
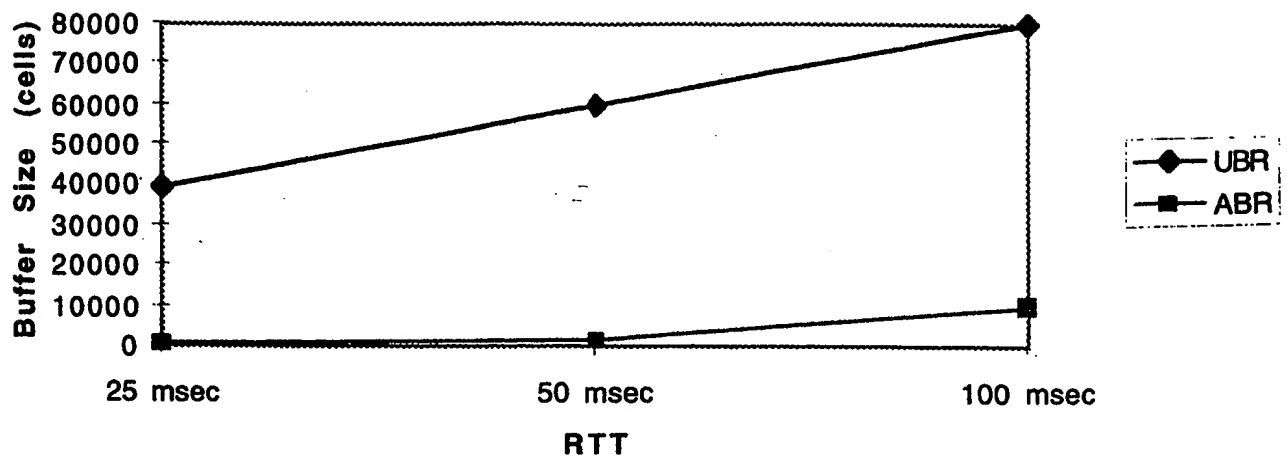


Figure 7 • UBR Goodput with Various Switch Buffer Sizes



**Figure 8 • Switch Buffer Requirements to Achieve Equivalent Goodput Performance**